

EL DISPLAY AND ITS DRIVING METHOD

Publication number: WO2004100118

Publication date: 2004-11-18

Inventor: TAKAHARA HIROSHI (JP)

Applicant: TOSHIBA MATSUSHITA DISPLAY TEC (JP); TAKAHARA HIROSHI (JP)

Classification:

- international: G09G3/20; G09G3/30; G09G3/20; G09G3/30; (IPC1-7): G09G3/30; G09G3/20

- European:

Application number: WO2004JP06153 20040428

Priority number(s): JP20030129528 20030507; JP20030277166 20030718; JP20040045517 20040220

Also published as:

EP1624435 (A1)
 US2007080905 (A)
 KR20070024733 (A)
 KR20060018831 (A)
 TW258113B (B)

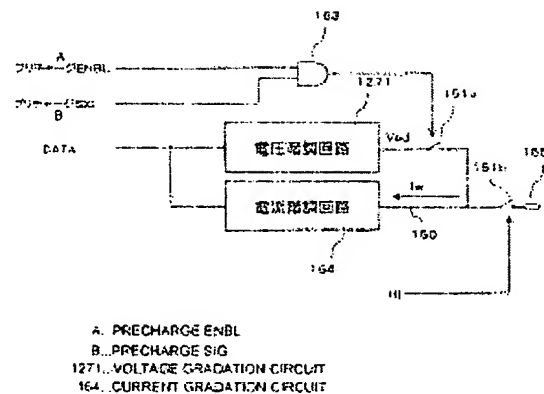
Cited documents:

JP2003114644
 JP2003108066
 JP2003108065
 JP2003099001
 JP2003066908
 more >>

Report a data error here

Abstract of WO2004100118

Conventionally, it has been difficult to display a favorable image by using an organic EL display panel. An EL display, according to the invention, comprises EL elements (15) arranged in a matrix, driving transistors (11a), and drive circuit means. The drive circuit means has a voltage gradation circuit (1271) for generating a program voltage signal, a current gradation circuit (164) for generating a program current signal, and switches (151a, 151b) for switching between the program voltage and current signals. The drive circuit is adapted to apply signals to the driving transistors (11a).



Data supplied from the esp@cenet database - Worldwide